

PATENT NUMBER _____

ISSUE CLASSIFICATION	
Class	Subclass

O.I.P.E.

PATENT DATE

12W O.I.P.E.
SCANNED MS ③ Q.A. [Signature]

APPLICATION NO. 09/746815	CONT/PRIOR	CLASS 711	SUBCLASS 101 102	ART UNIT 2485	EXAMINER <u>H. K.</u> B. Koush
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APPLICANTS

Multiple memory aliasing for a configurable system-on-chip

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner) _____ (Date)		NOTICE OF ALLOWANCE MAILED 	
	_____ (Primary Examiner) _____ (Date)		ISSUE FEE Amount Due _____ Date Paid _____	
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